

FIG. 1

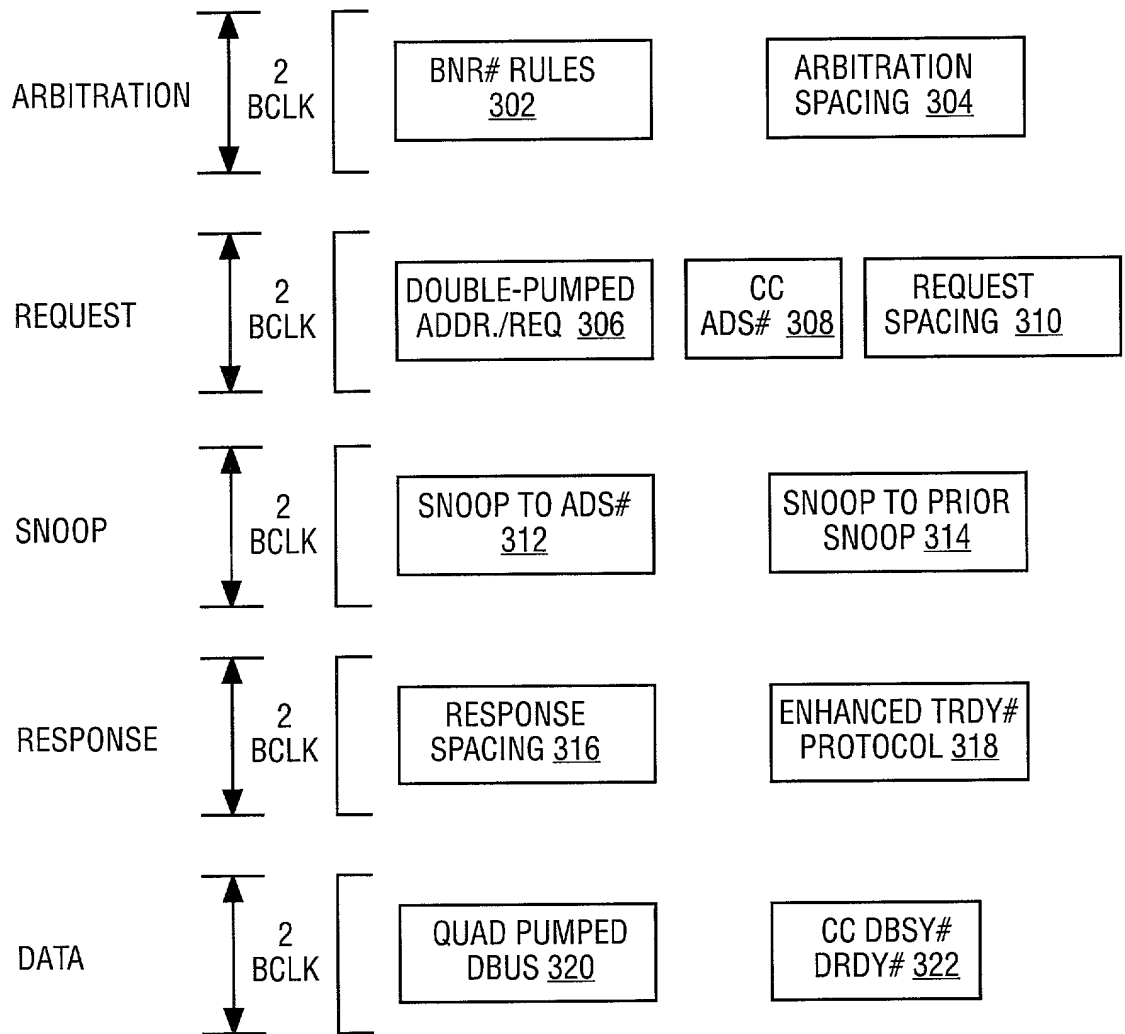
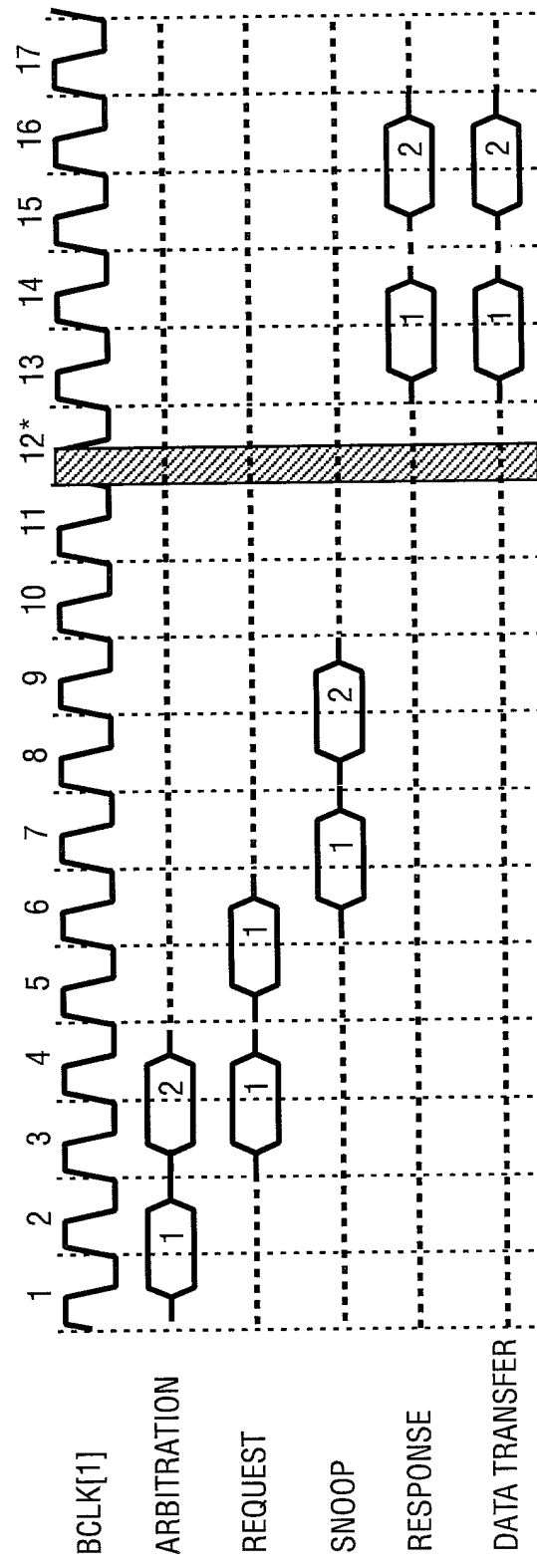


FIG. 2



*NOTE: THE SHADED VERTICAL BAR INDICATES ONE OR MORE CLOCK CYCLES ARE ALLOWED BETWEEN DIFFERENT PHASES.

FIG. 3A

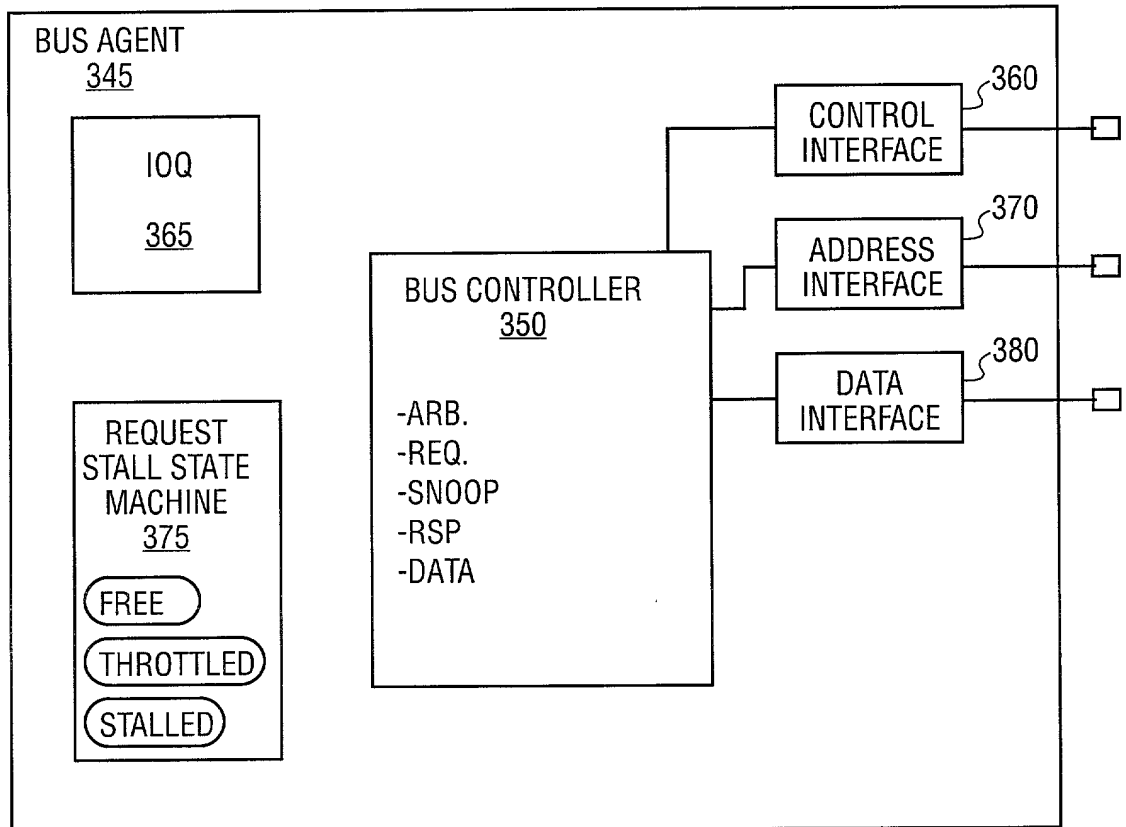


FIG. 3B

FIG. 4

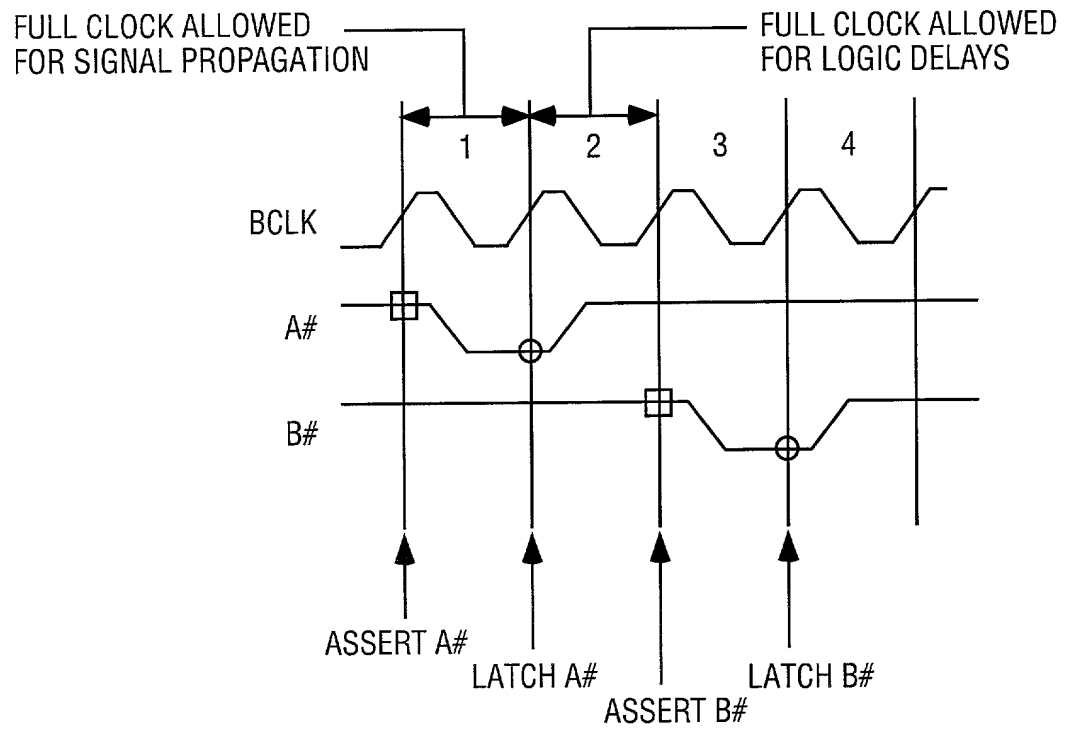


FIG. 4

FULL CLOCK ALLOWED
FOR SIGNAL PROPAGATION

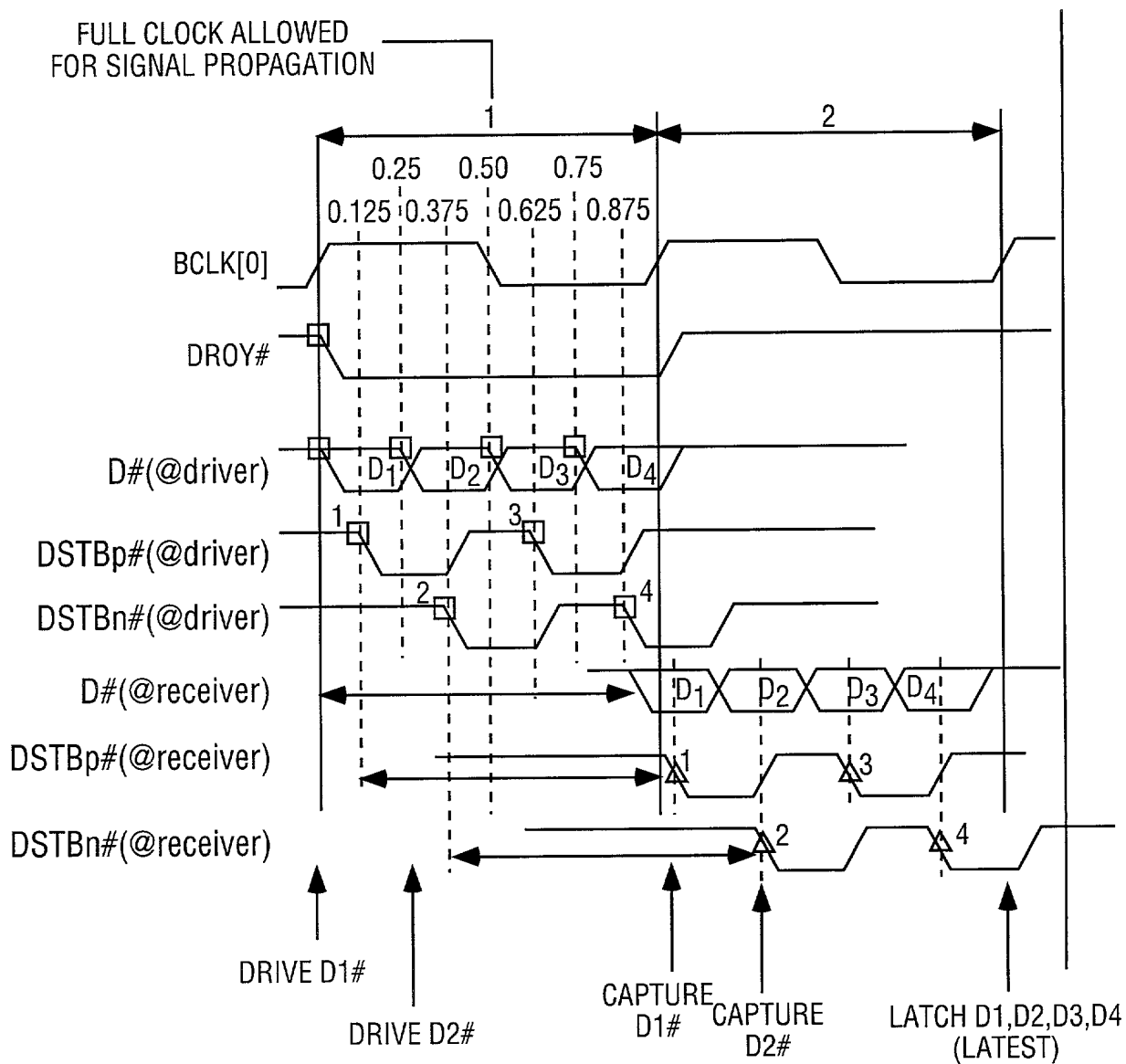


FIG. 5A

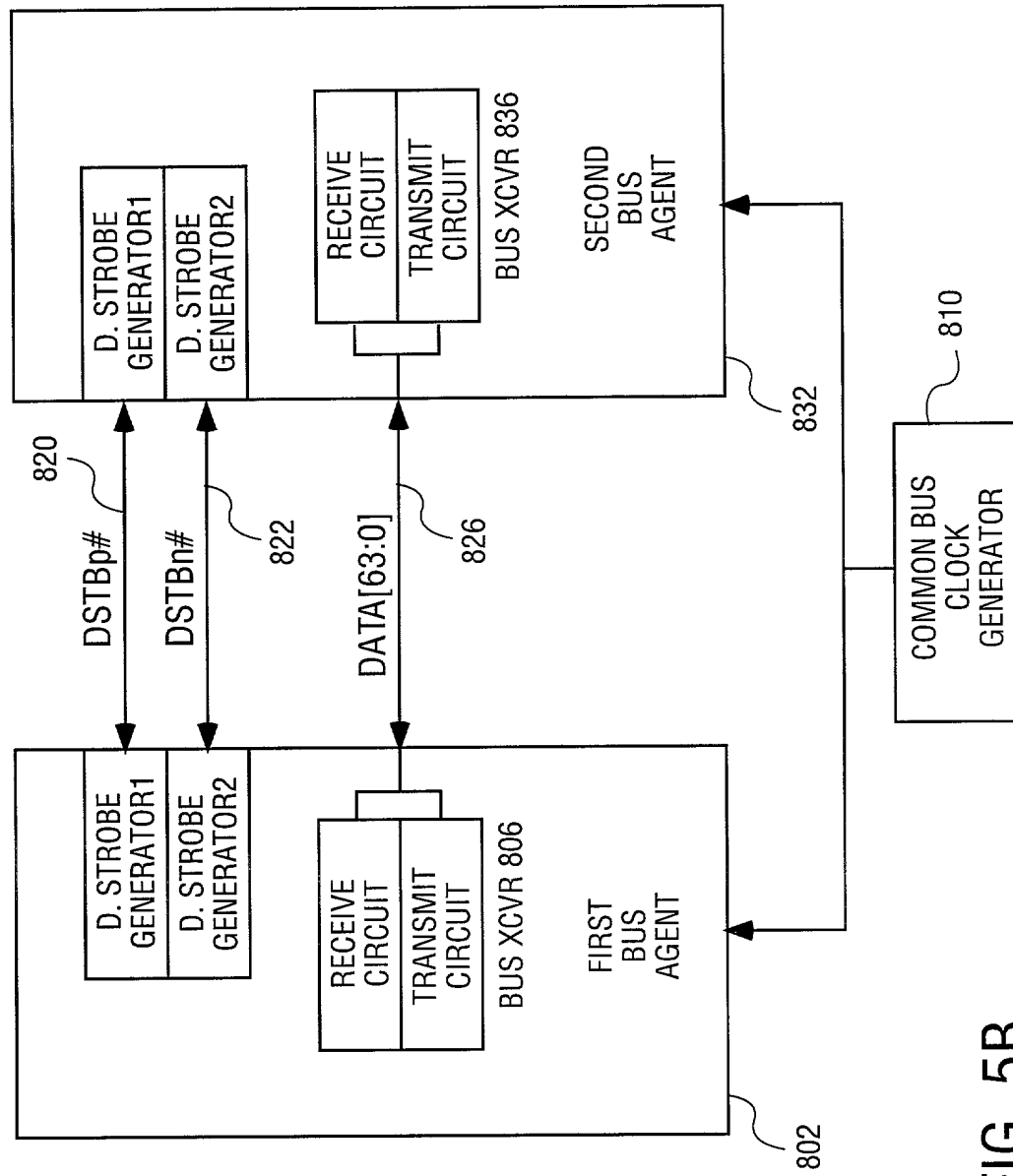


FIG. 5B

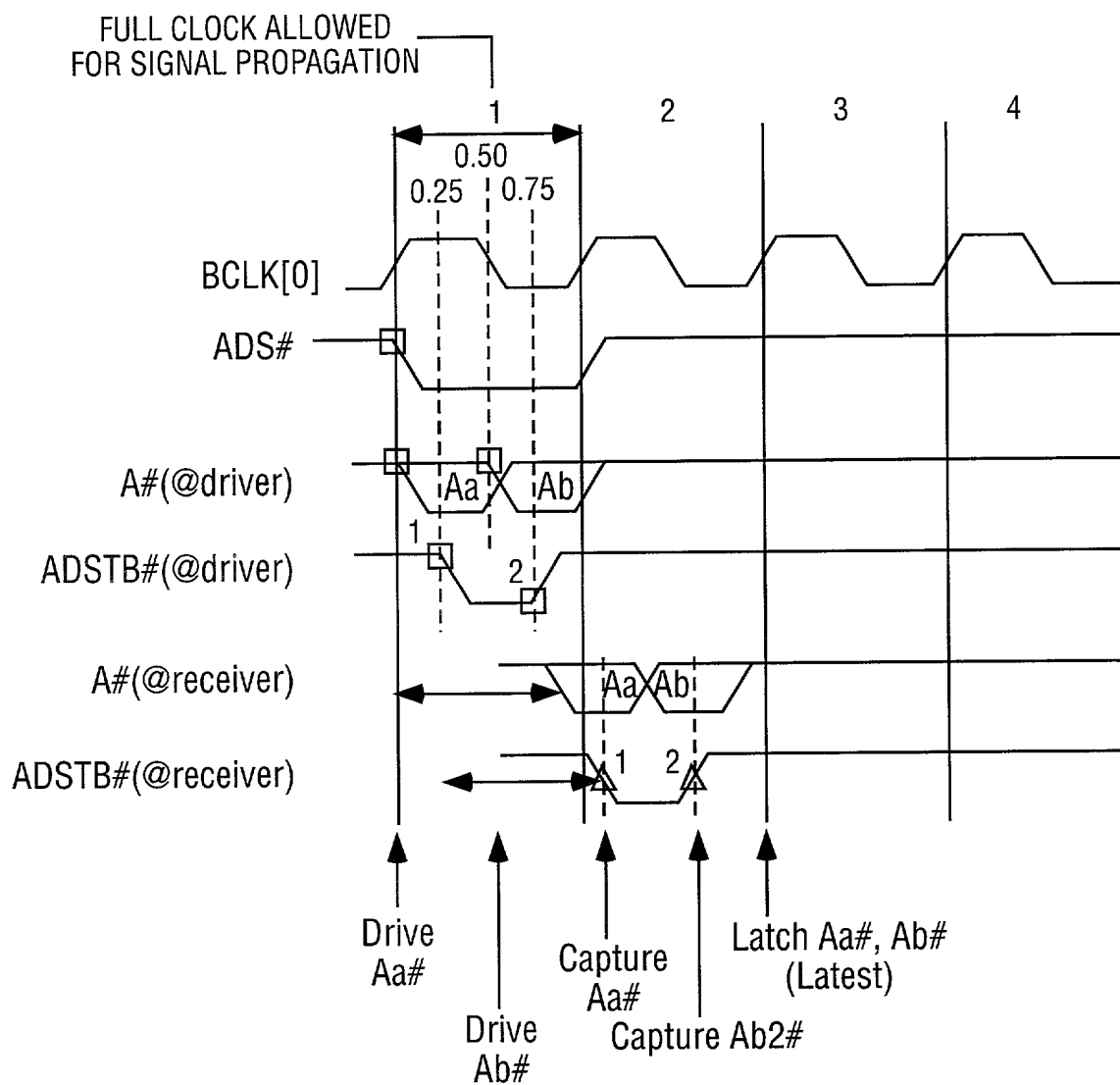


FIG. 6A

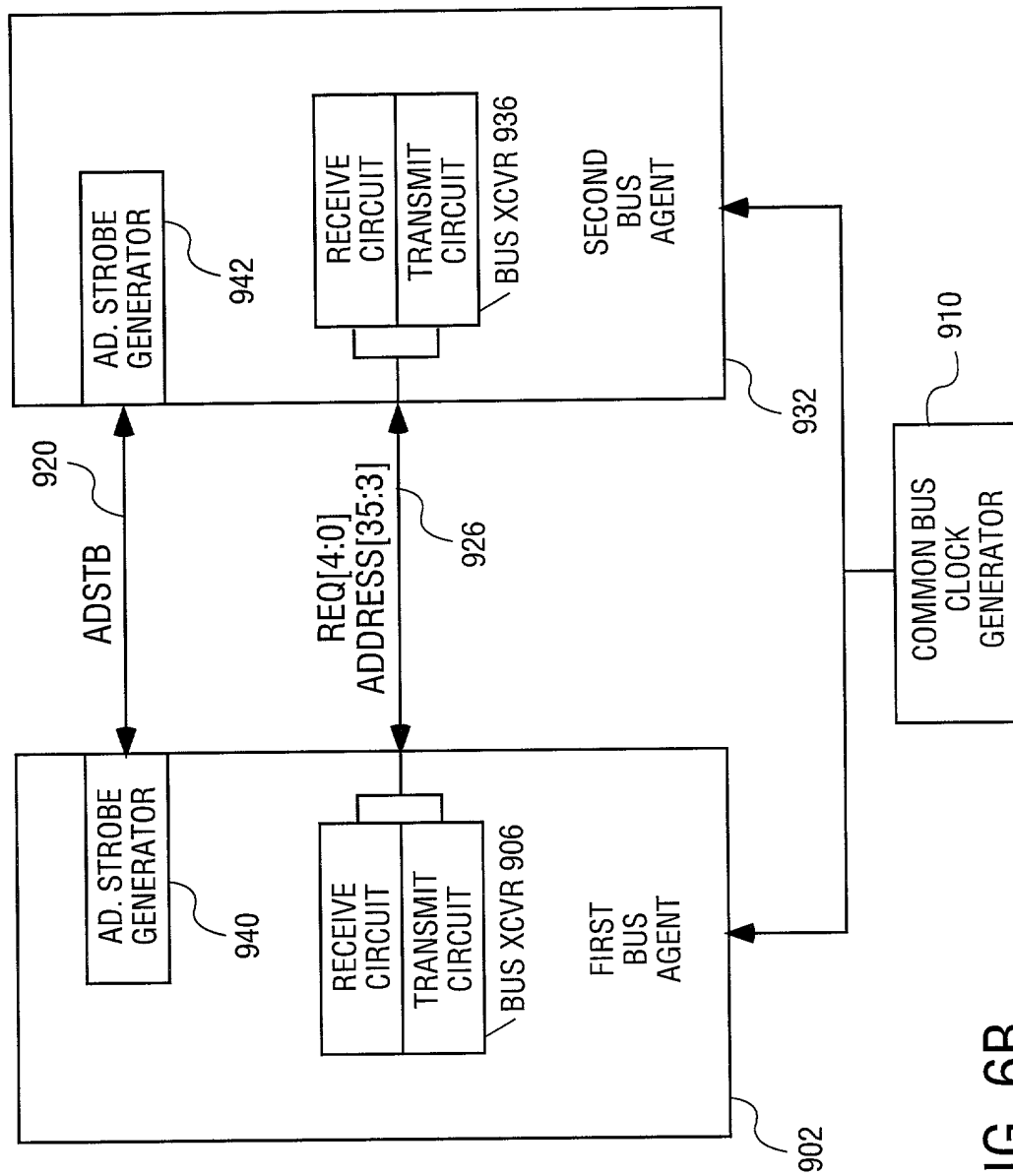


FIG. 6B

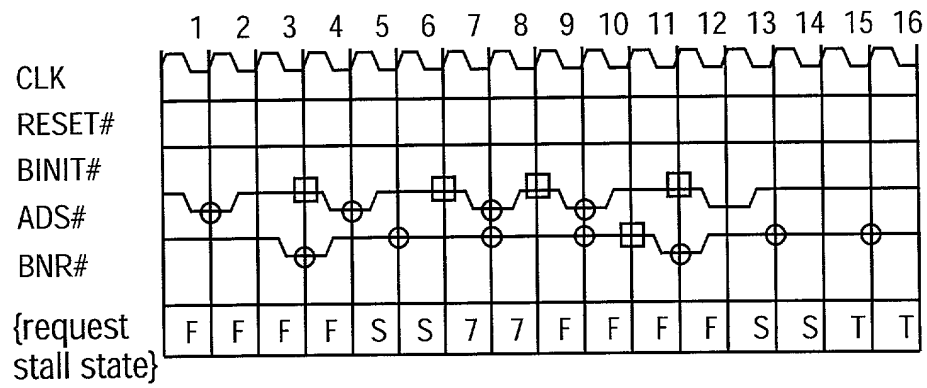


FIG. 8

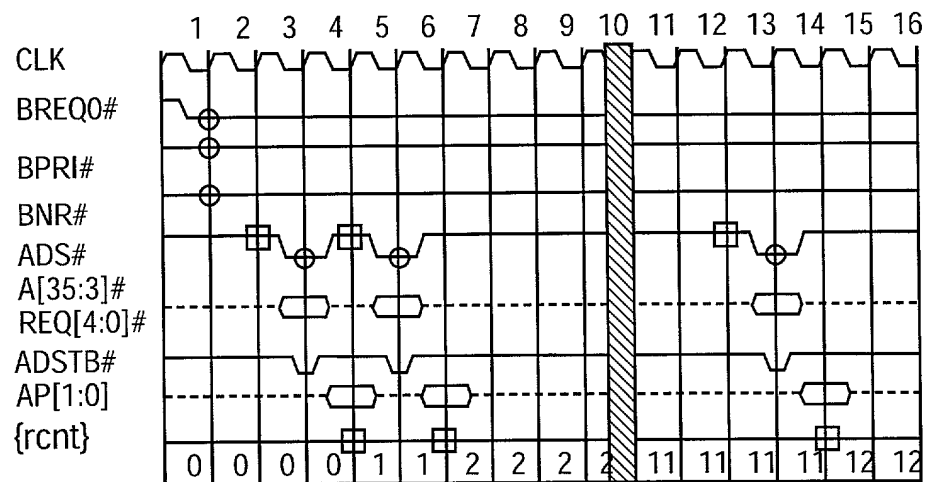


FIG. 9

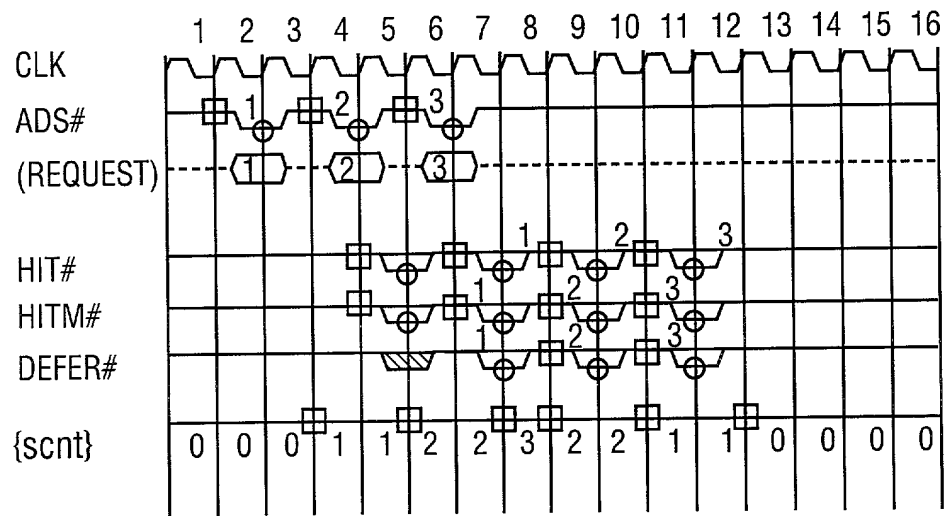


FIG. 10

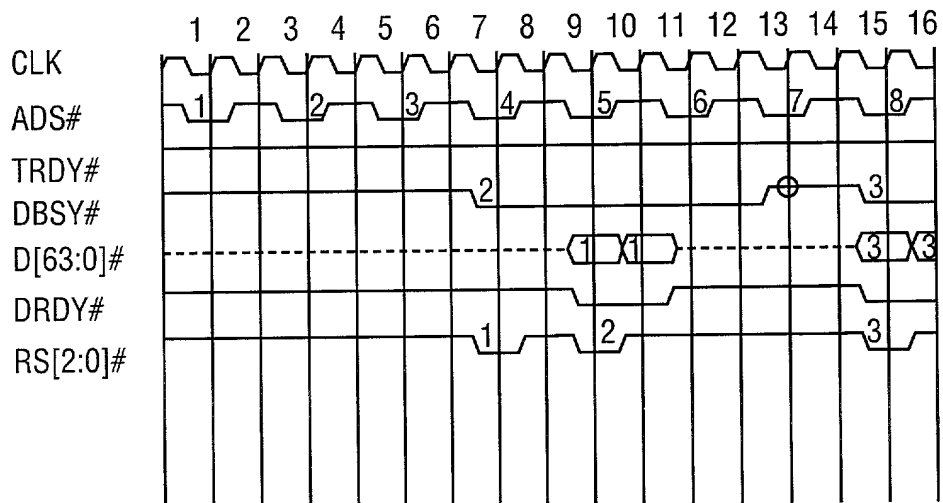


FIG. 11

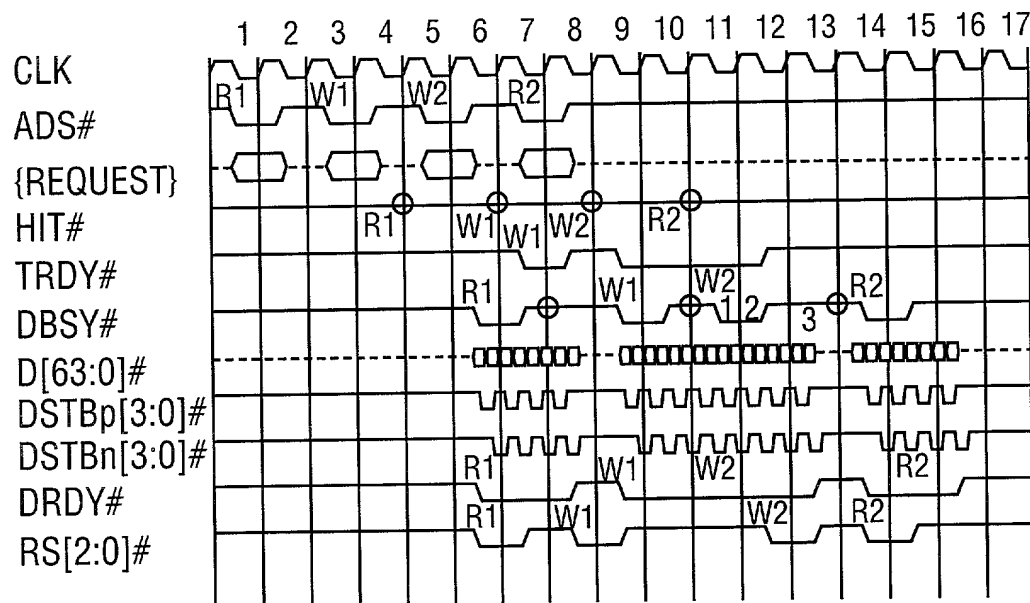


FIG. 12

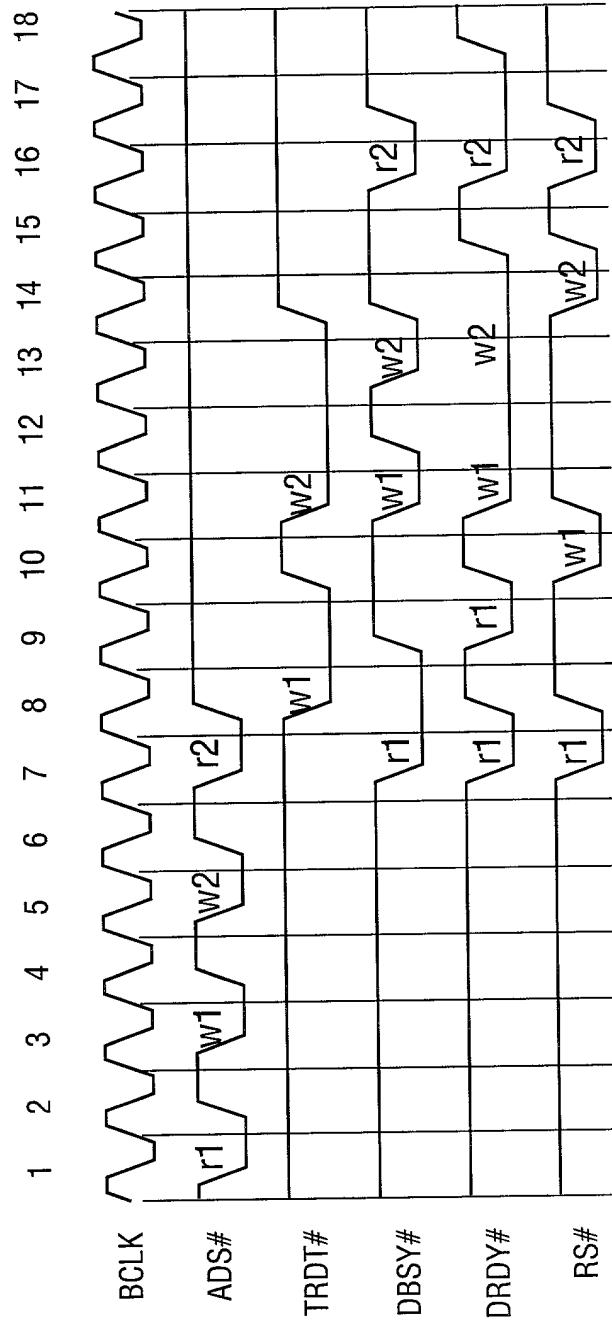


FIG. 13

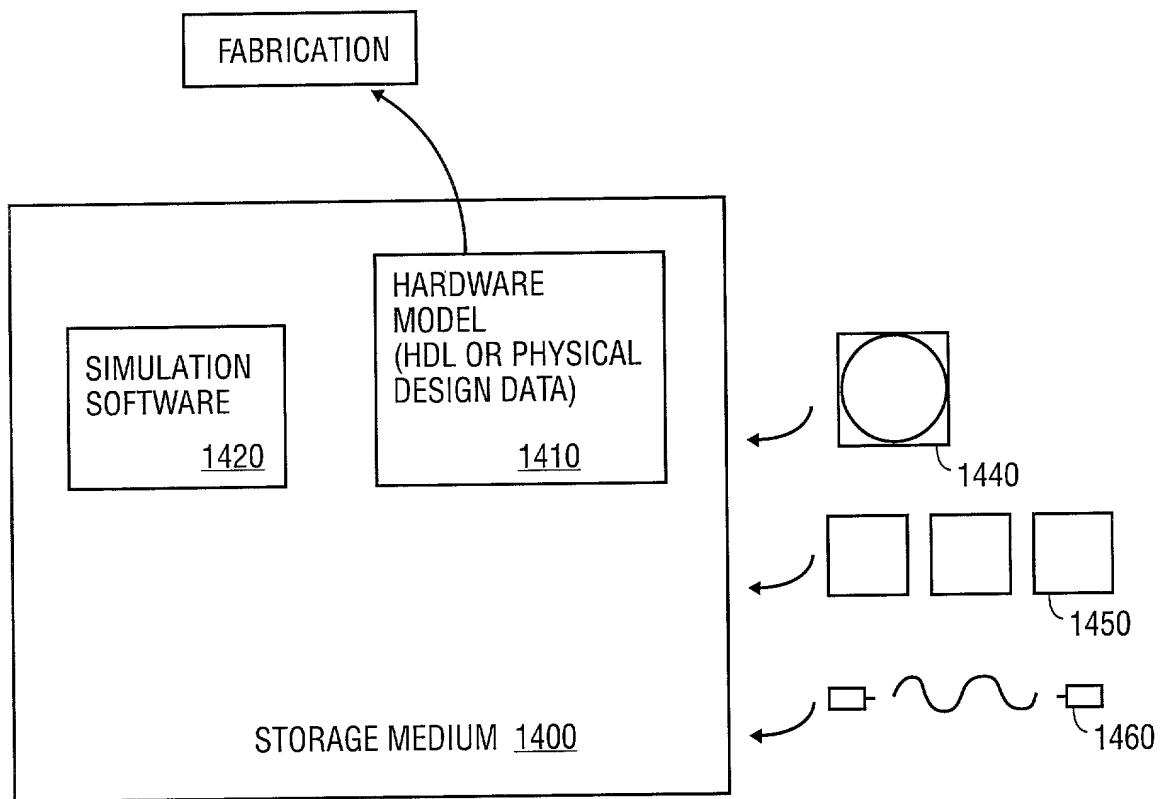


FIG. 14